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To: Jezia Riley, Examiner

From: Ben Liu

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June 23, 2004

Jezia Riley, Examiner
USPTO
Remsen Building, 400 Dulang St.
Alexandria, VA 22314-5774

Dear Examiner Riley:

Thank you very much for talking with me on the phone regarding Patent Application #10/055878 entitled Four dimensional biochip design for high throughput applications and methods of using the four dimensional biochip. The attached is our official responses to the Office Action date mailed 05/05/2004. The responses include a brief explanation, a claim listing, and the amended claims.

Many thanks for your help.



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